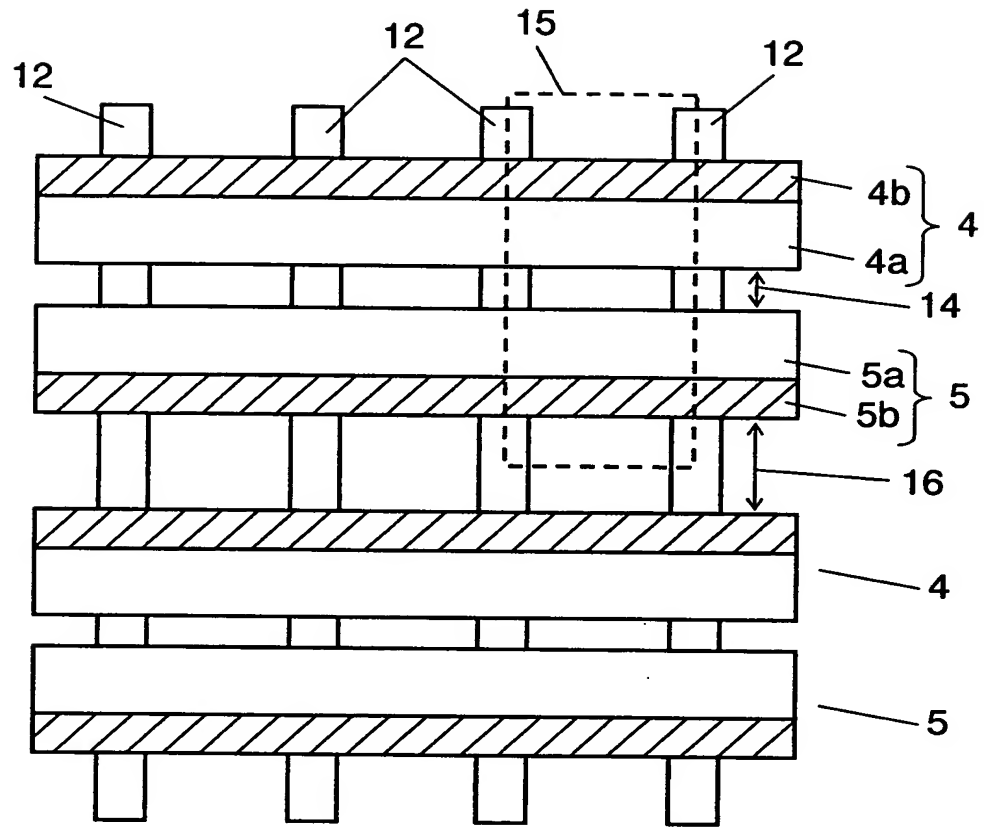


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FIG. 2



Reference marks in the drawings

- 1** front panel
- 2** back panel
- 3** front substrate
- 4** scanning electrode
- 4a, 5a** transparent electrode
- 4b, 5b** bus electrode
- 5** sustaining electrode
- 6** display electrode
- 7** dielectric layer
- 8** protective layer
- 9** back substrate
- 10** address electrode
- 11** base dielectric layer
- 12** barrier rib
- 13** phosphor layer
- 14** discharge gap
- 15** discharge cell